Toppan Printing

FIG.1

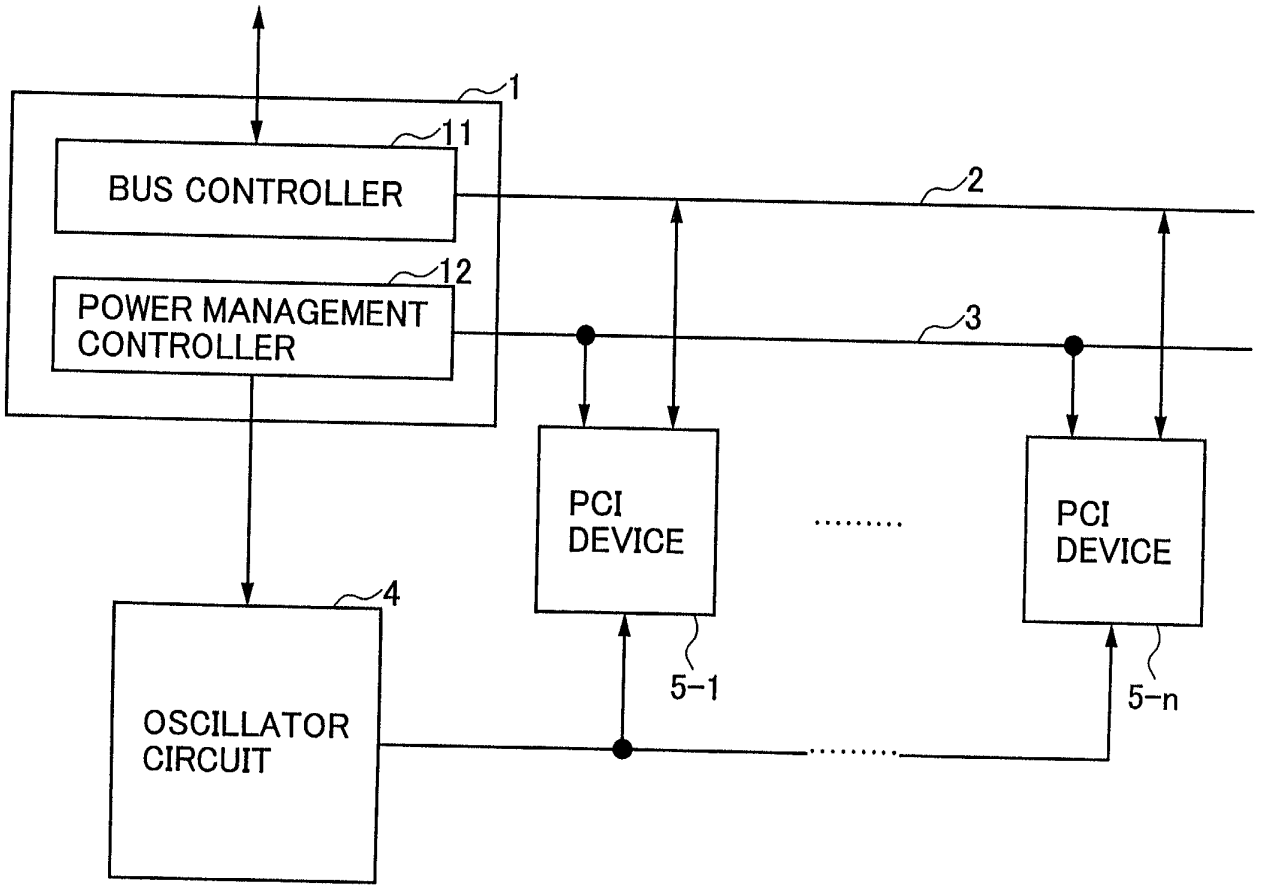


FIG.2

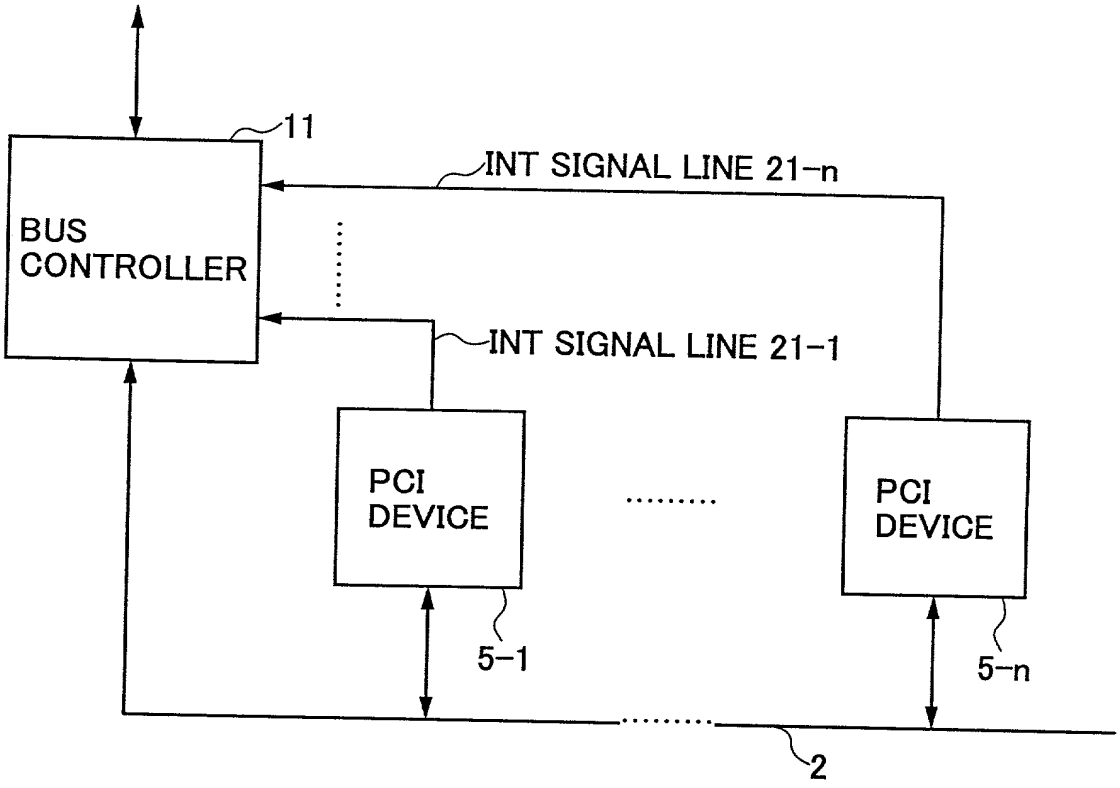
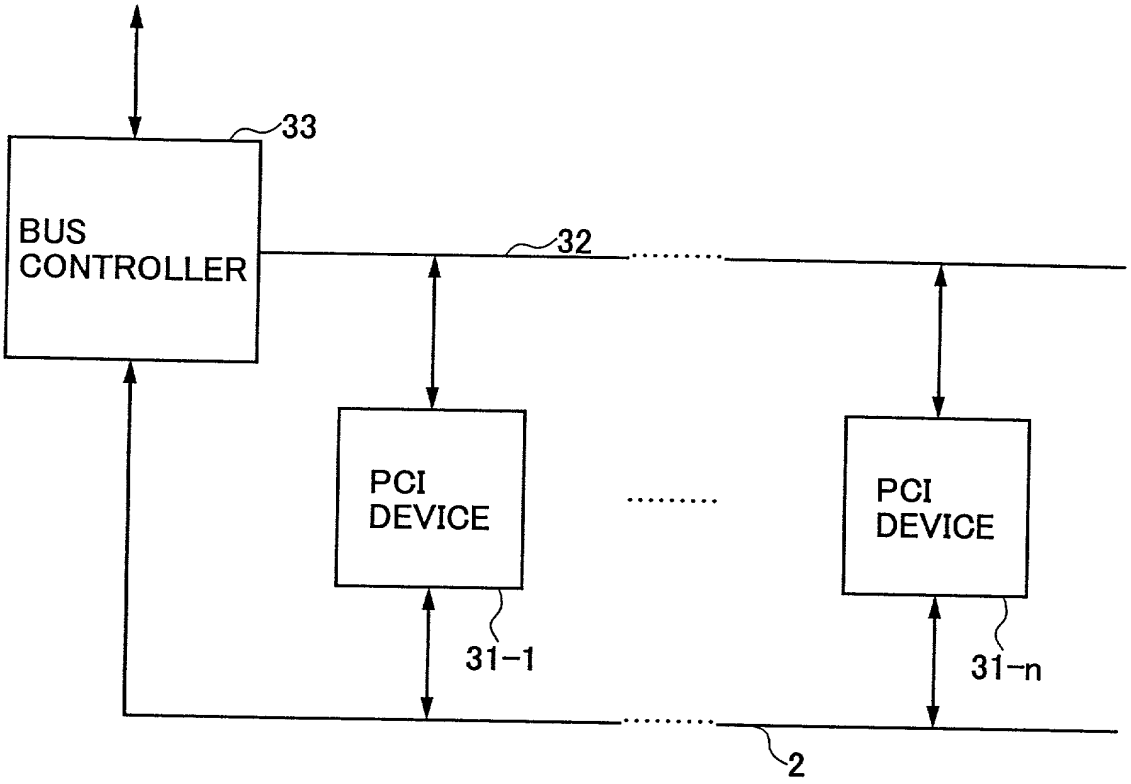

11
INT SIGNAL LINE 21-n
BUS
CONTROLLER
INT SIGNAL LINE 21-1
PCI
DEVICE
PCI
DEVICE
5-1
5-n
2

FIG.3



TOSHIBA ASSIGNED

FIG.4

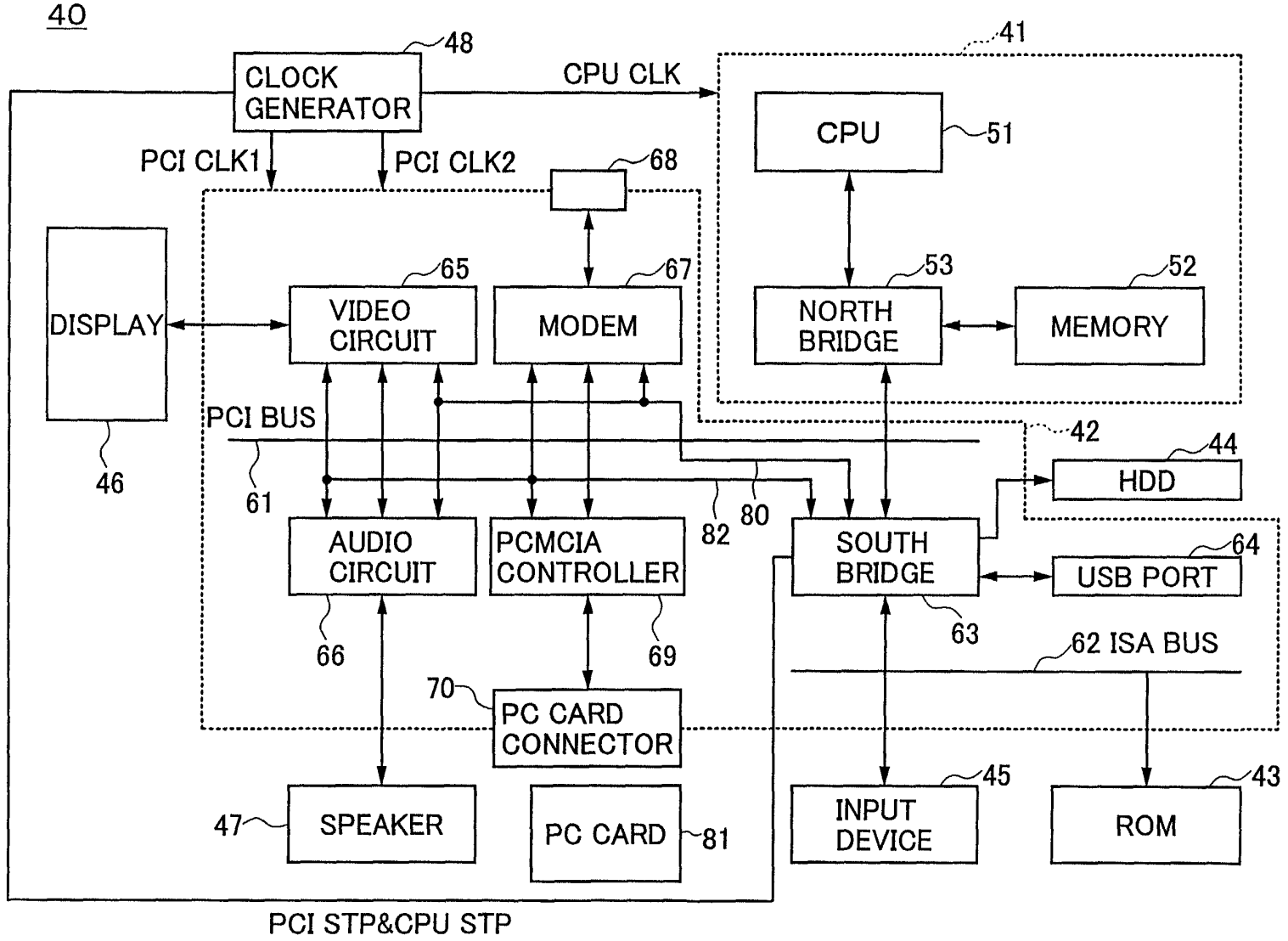


FIG.5

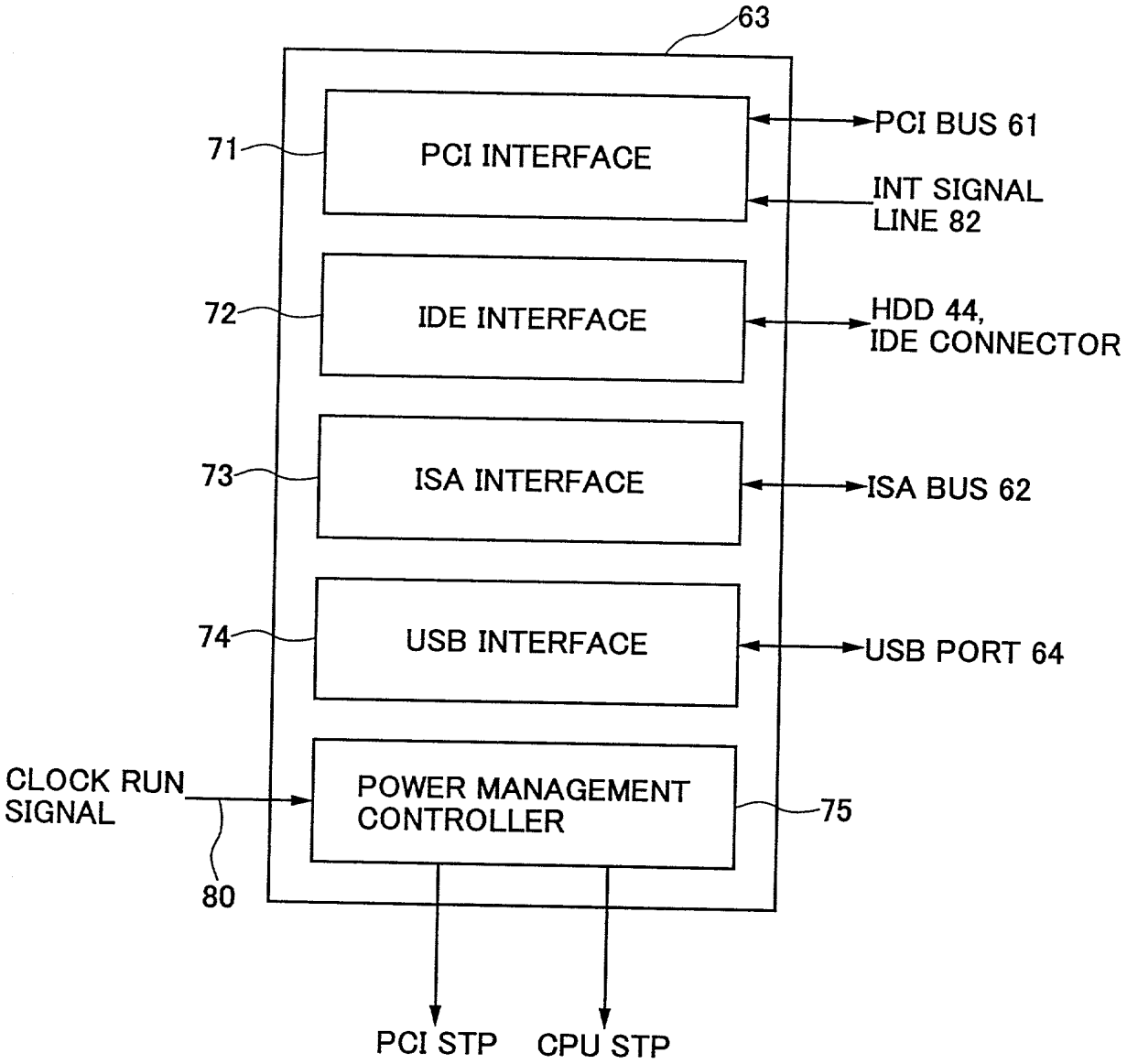

63
71
PCI INTERFACE
PCI BUS 61
INT SIGNAL LINE 82
72
IDE INTERFACE
HDD 44, IDE CONNECTOR
73
ISA INTERFACE
ISA BUS 62
74
USB INTERFACE
USB PORT 64
CLOCK RUN SIGNAL
POWER MANAGEMENT CONTROLLER
75
80
PCI STP
CPU STP

FIG.6

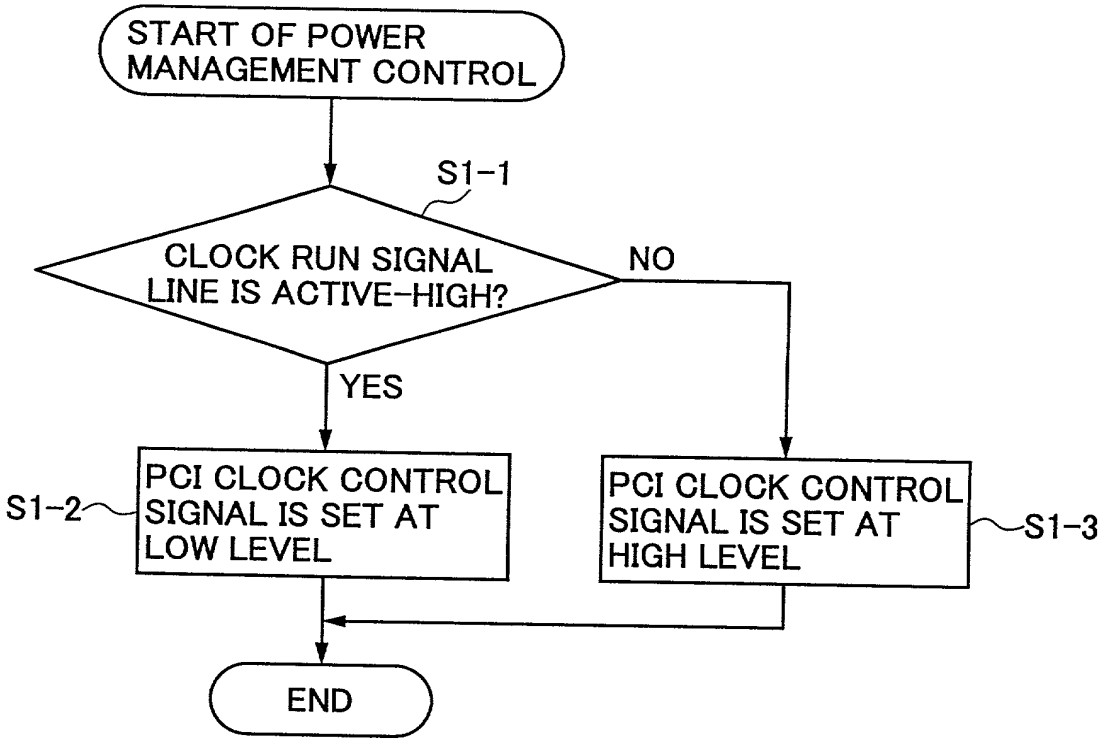
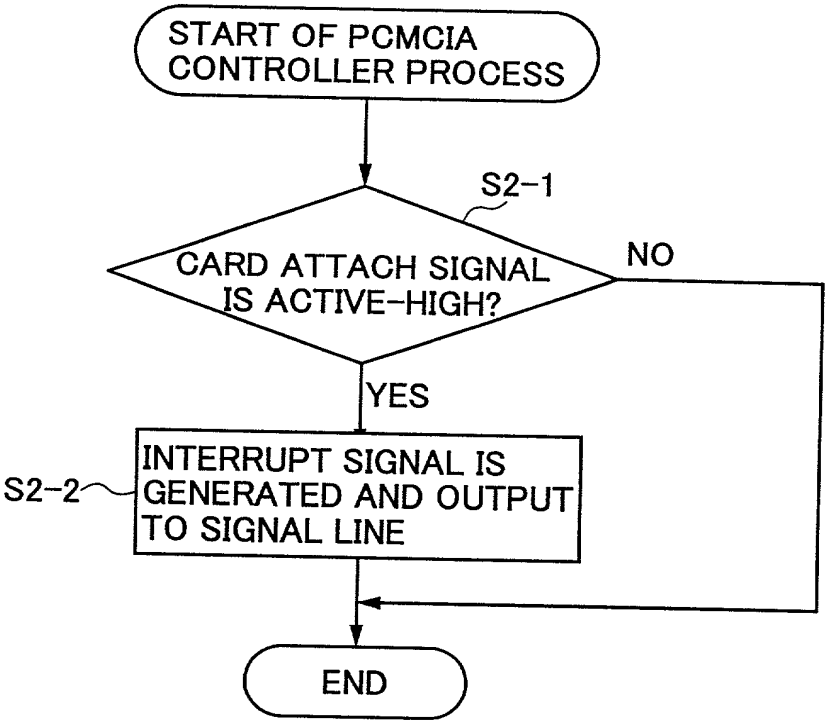

START OF POWER MANAGEMENT CONTROL
S1-1
CLOCK RUN SIGNAL LINE IS ACTIVE-HIGH?
NO
YES
S1-2
PCI CLOCK CONTROL SIGNAL IS SET AT LOW LEVEL
PCI CLOCK CONTROL SIGNAL IS SET AT HIGH LEVEL
S1-3
END

FIG.7


START OF PCMCIA CONTROLLER PROCESS
S2-1
CARD ATTACH SIGNAL IS ACTIVE-HIGH?
NO
YES
S2-2
INTERRUPT SIGNAL IS GENERATED AND OUTPUT TO SIGNAL LINE
END

48

FIG.8

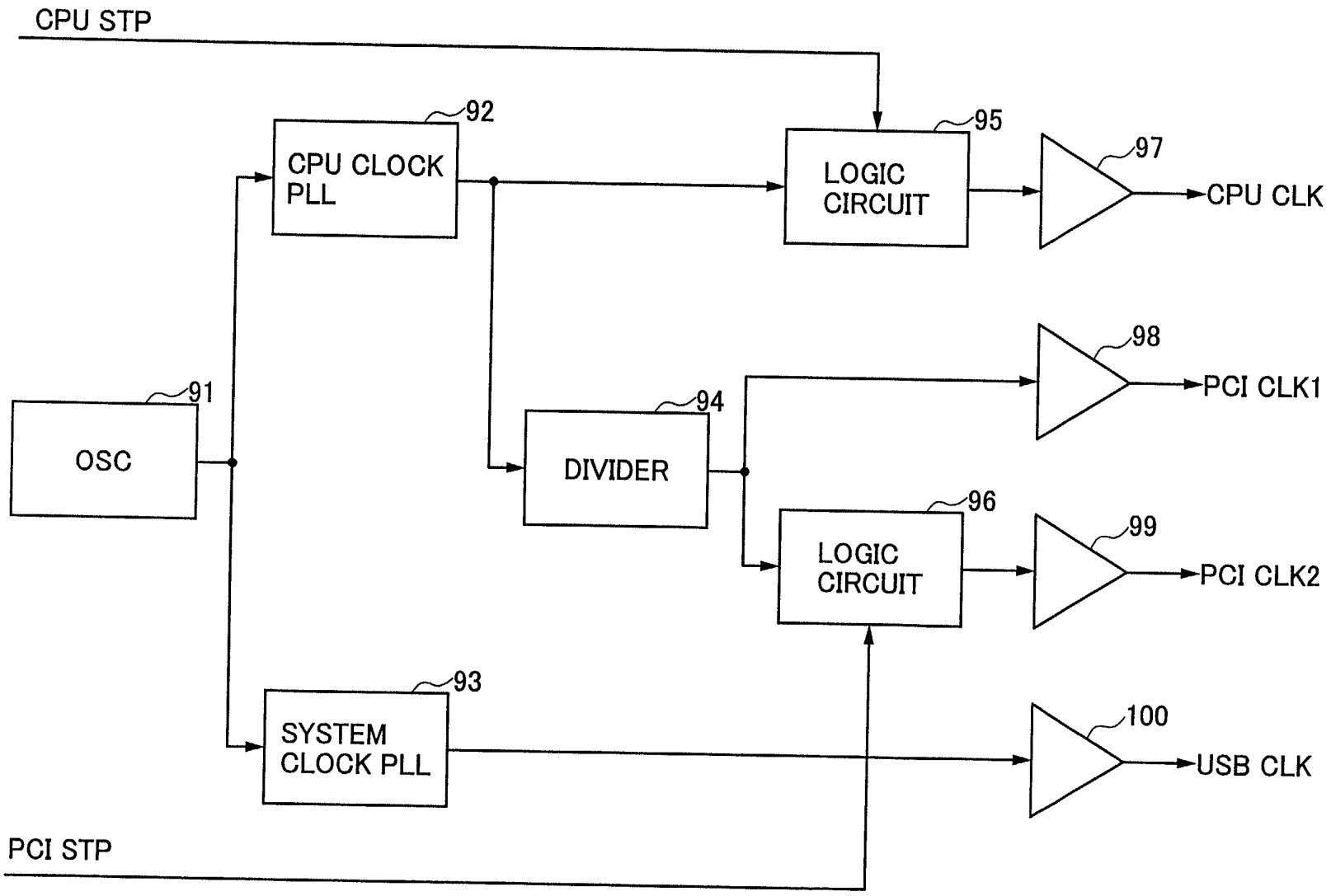
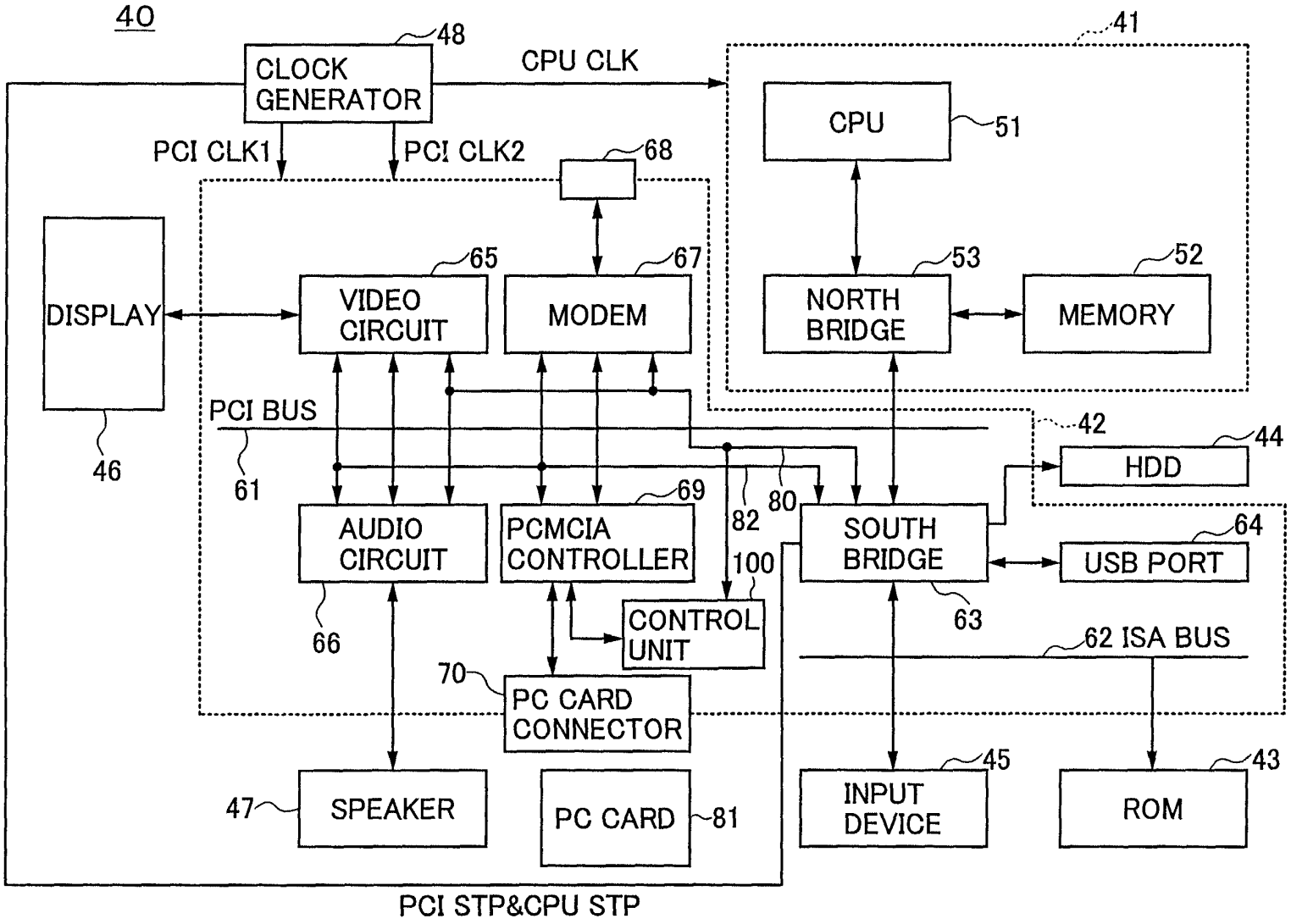
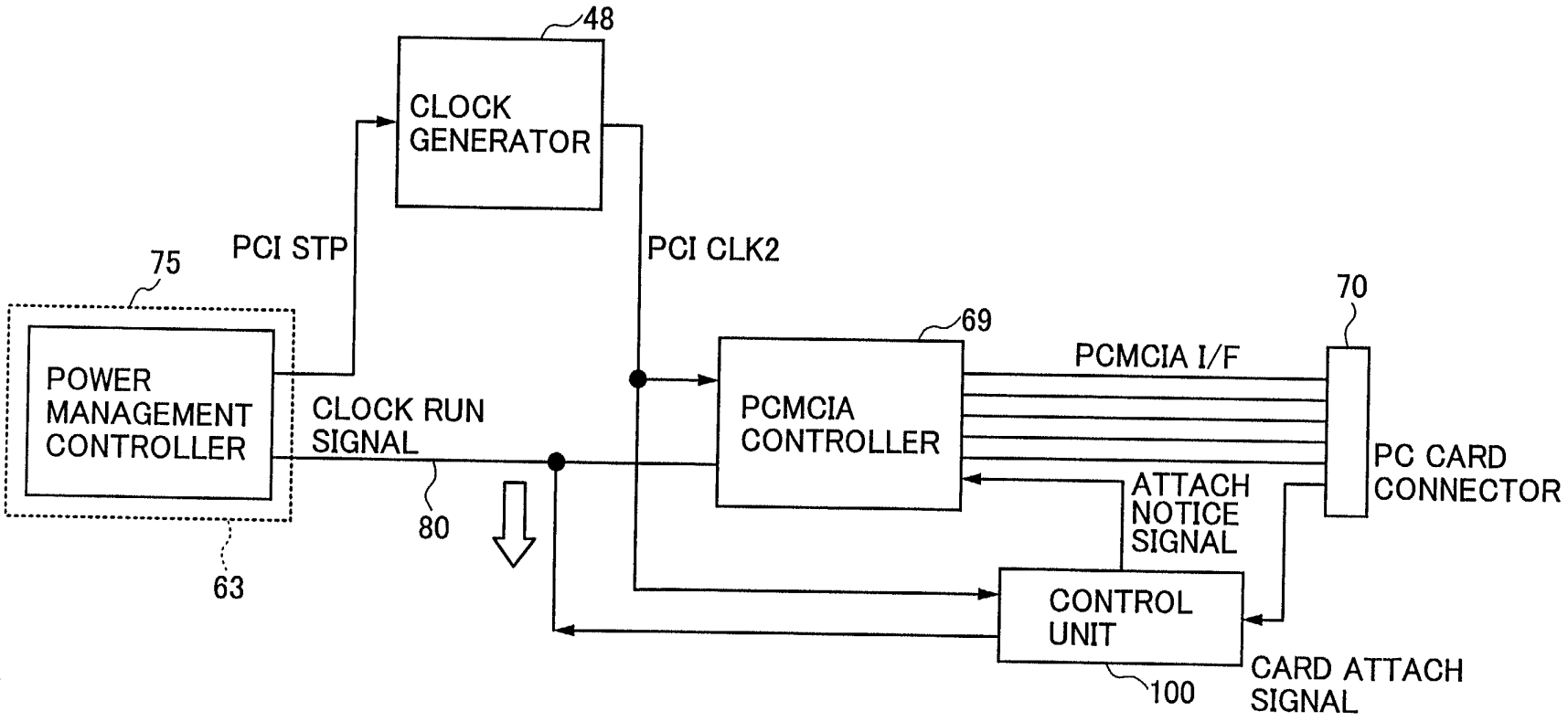

CPU STP
92
CPU CLOCK PLL
95
LOGIC CIRCUIT
97
CPU CLK
98
PCI CLK1
91
OSC
94
DIVIDER
96
LOGIC CIRCUIT
99
PCI CLK2
93
SYSTEM CLOCK PLL
100
USB CLK
PCI STP

FIG.9


40
41
48
CLOCK GENERATOR
CPU CLK
CPU
51
PCI CLK1
PCI CLK2
68
DISPLAY
65
VIDEO CIRCUIT
67
MODEM
53
NORTH BRIDGE
52
MEMORY
PCI BUS
42
44
HDD
46
61
69
82
80
AUDIO CIRCUIT
PCMCIA CONTROLLER
SOUTH BRIDGE
64
USB PORT
100
66
CONTROL UNIT
63
62 ISA BUS
70
PC CARD CONNECTOR
45
43
47
SPEAKER
PC CARD
81
INPUT DEVICE
ROM
PCI STP&CPU STP

PUBLISHED DOCUMENT

FIG.10



PUBLISHED SEARCH

FIG.11

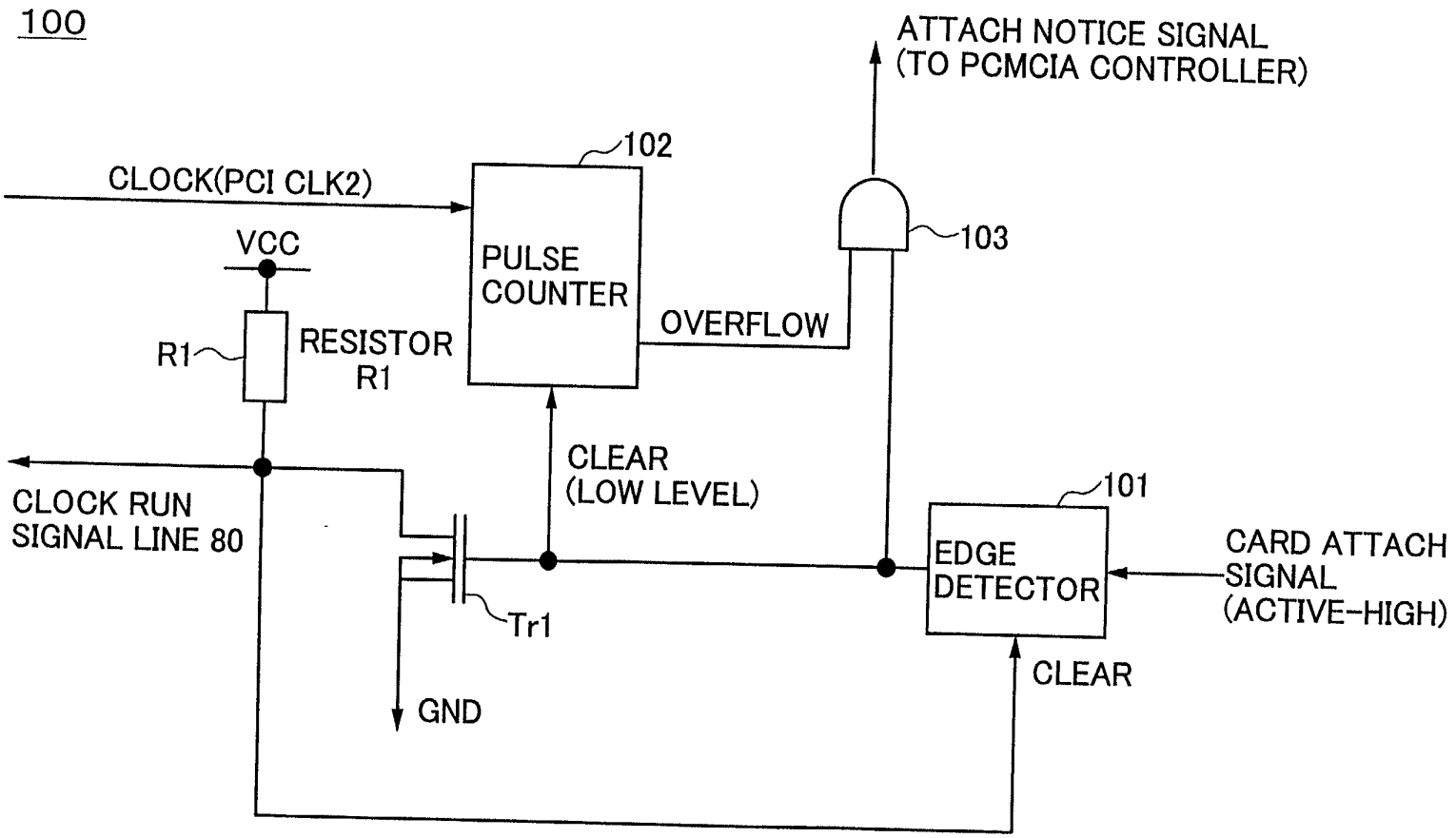


FIG.12

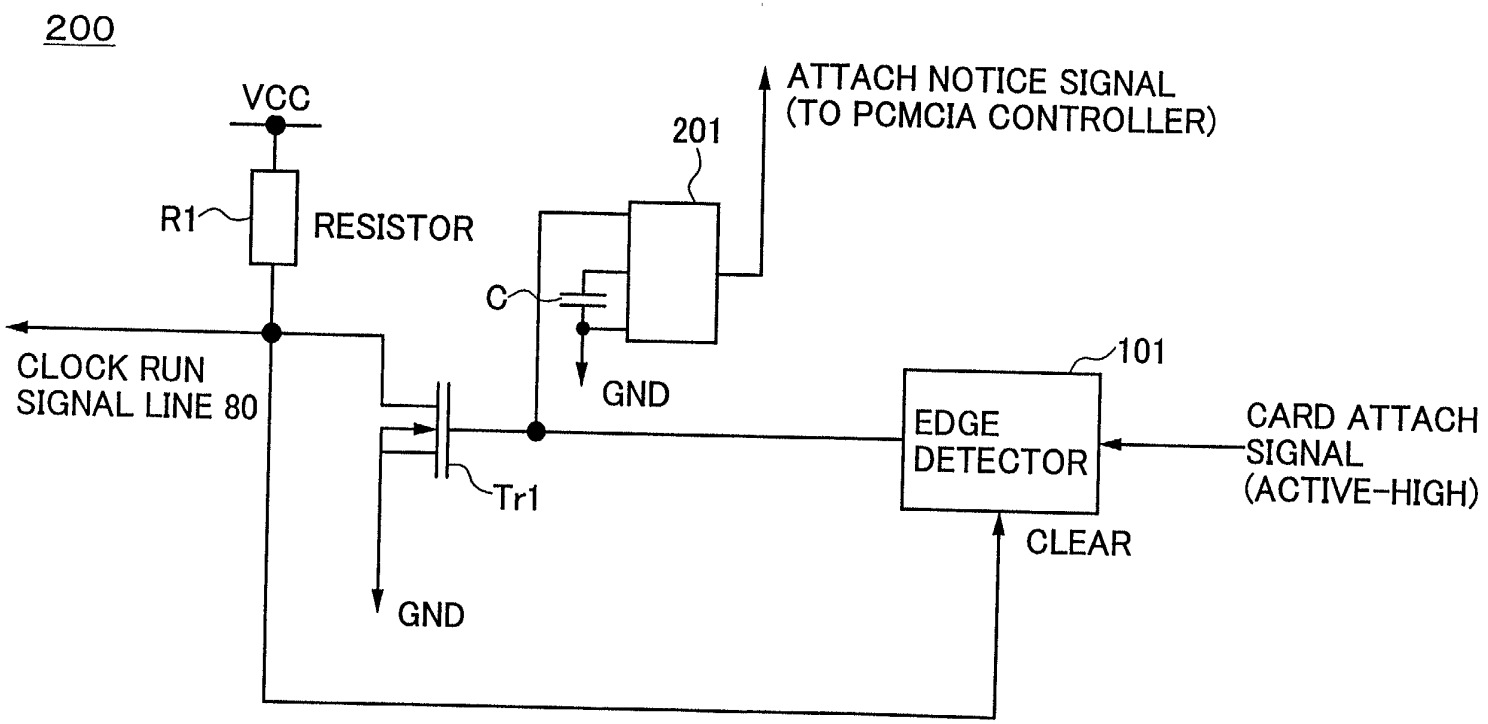
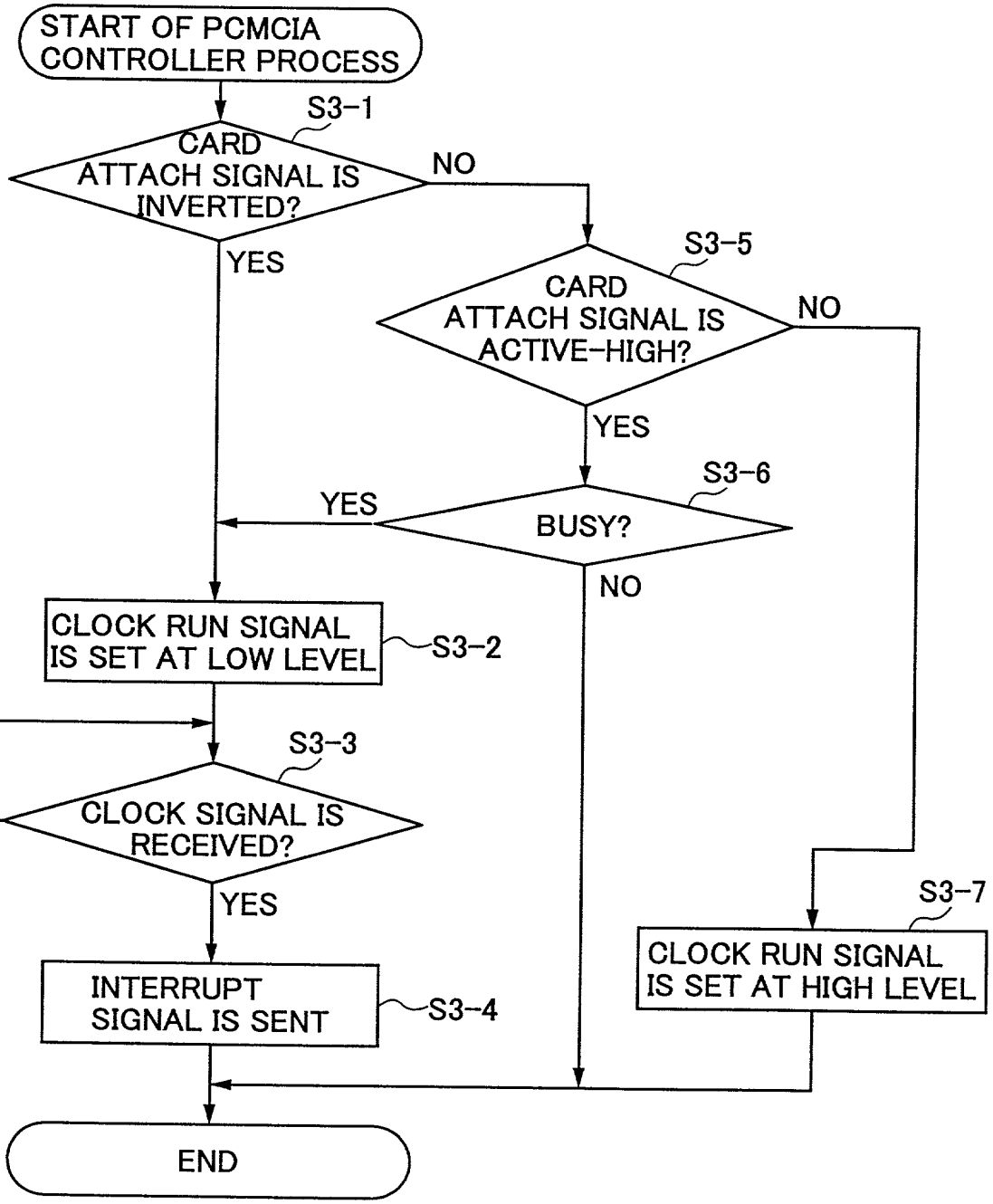

200
VCC
R1
RESISTOR
201
ATTACH NOTICE SIGNAL
(TO PCMCIA CONTROLLER)
C
GND
CLOCK RUN
SIGNAL LINE 80
Tr1
GND
101
EDGE
DETECTOR
CARD ATTACH
SIGNAL
(ACTIVE-HIGH)
CLEAR

FIG.13


START OF PCMCIA CONTROLLER PROCESS
S3-1
CARD ATTACH SIGNAL IS INVERTED?
NO
YES
S3-5
CARD ATTACH SIGNAL IS ACTIVE-HIGH?
NO
YES
S3-6
BUSY?
YES
NO
CLOCK RUN SIGNAL IS SET AT LOW LEVEL
S3-2
S3-3
NO
CLOCK SIGNAL IS RECEIVED?
YES
INTERRUPT SIGNAL IS SENT
S3-4
S3-7
CLOCK RUN SIGNAL IS SET AT HIGH LEVEL
END